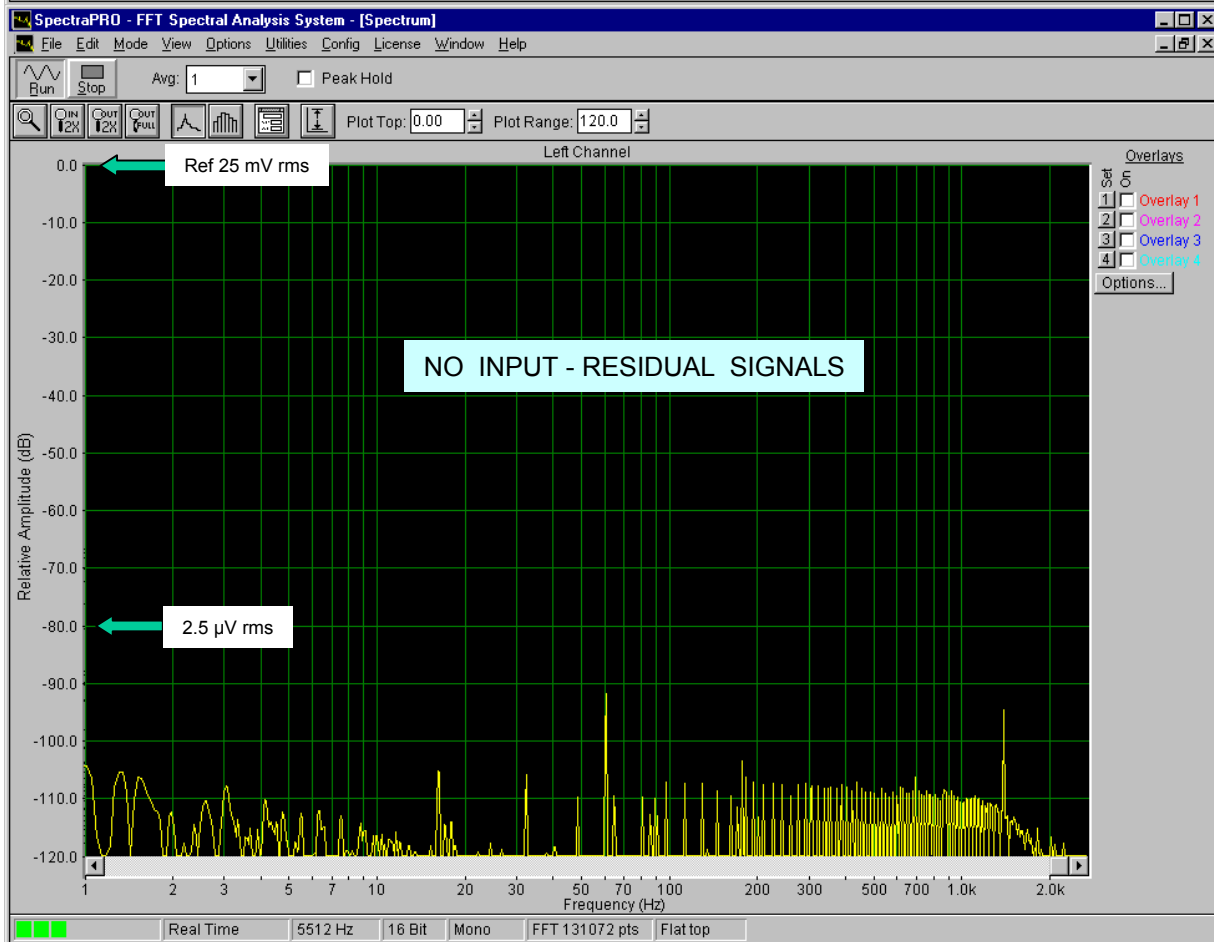
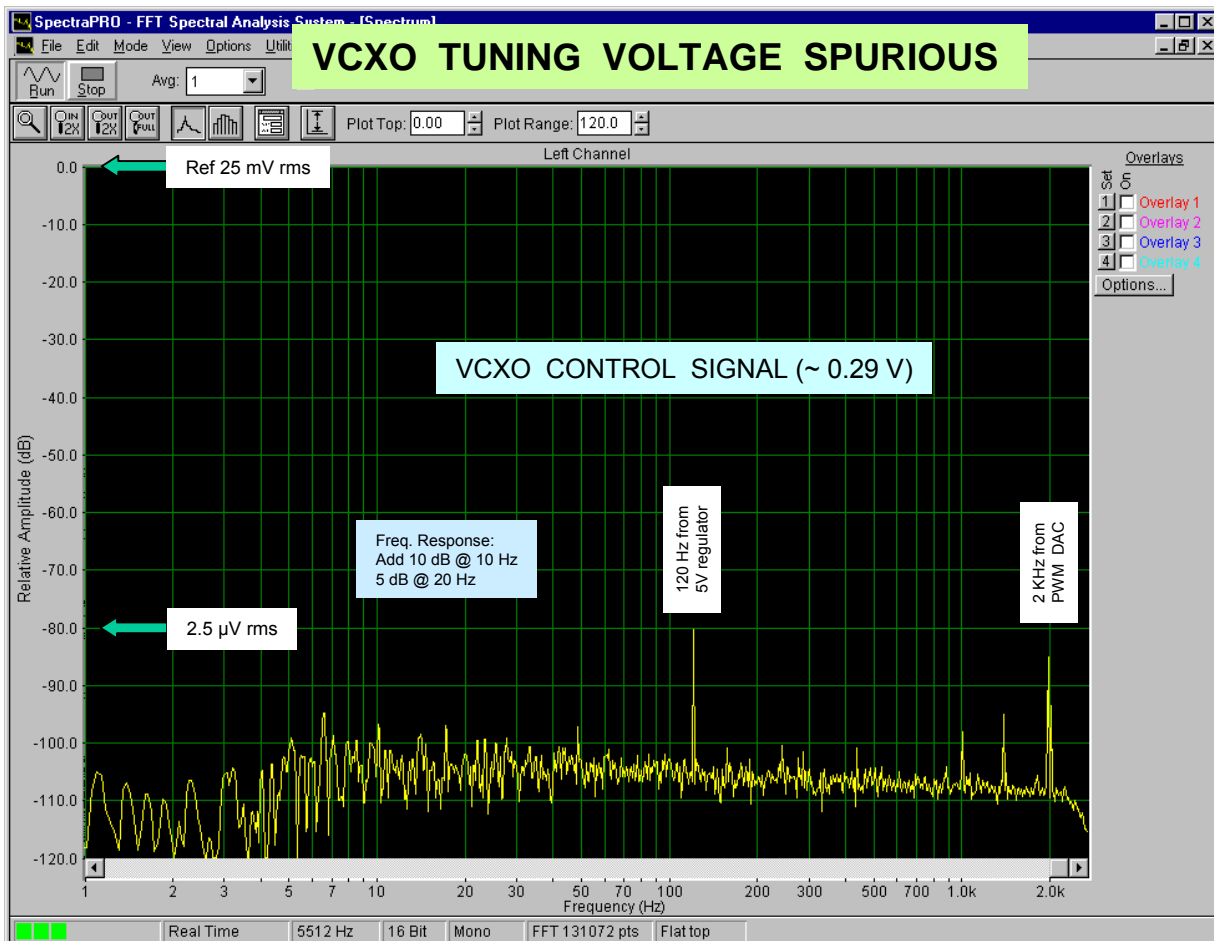
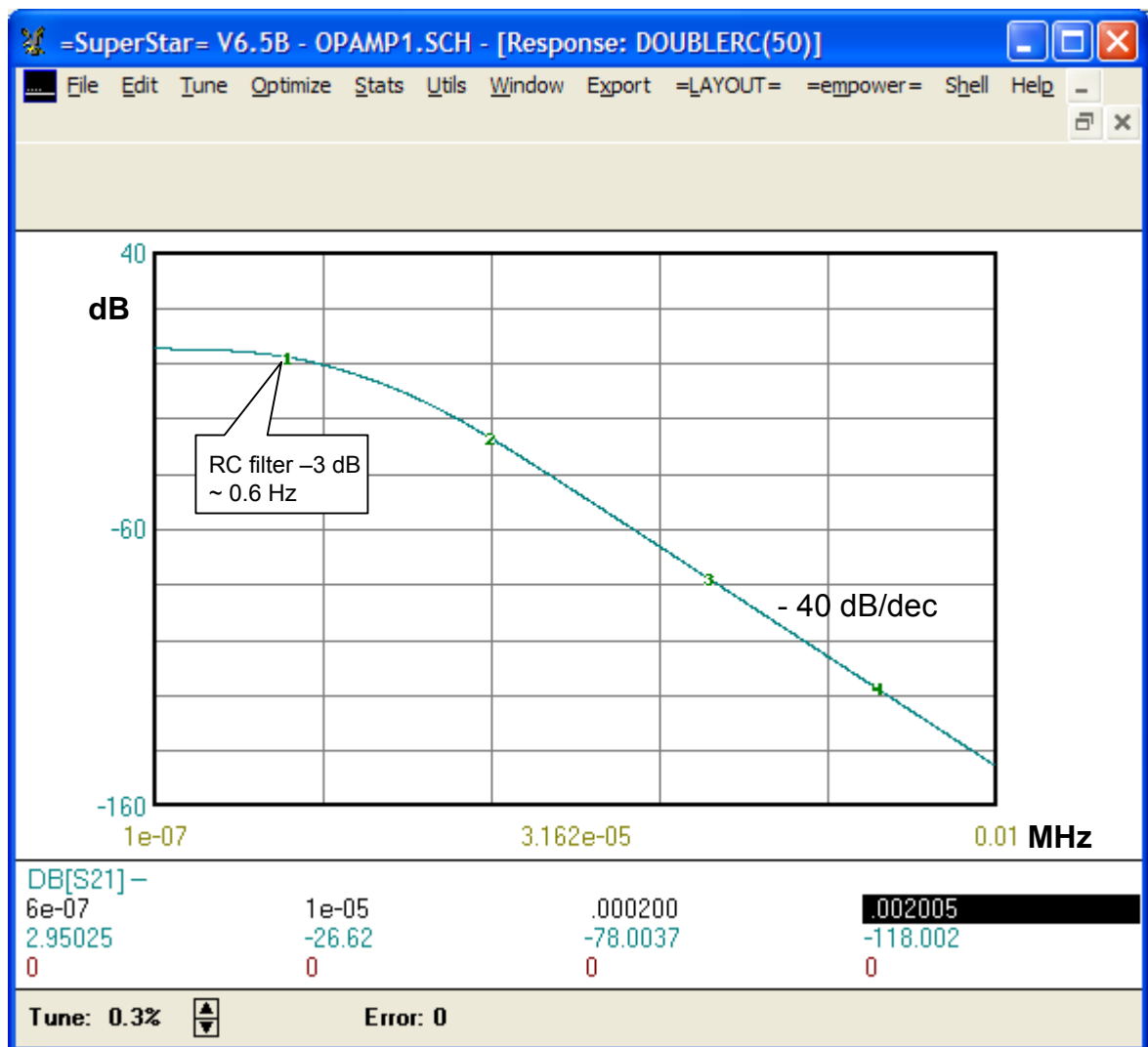
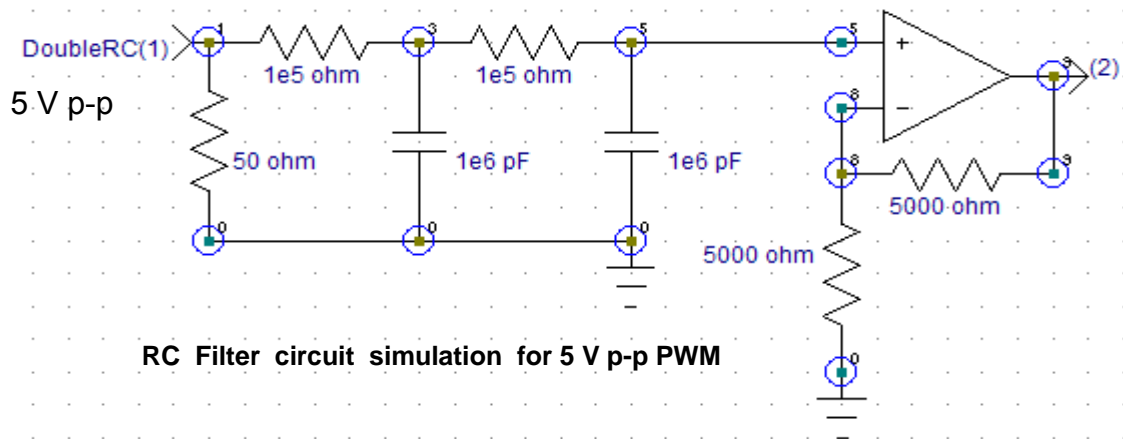


ANALYSIS OF VCXO AS PART OF GPS FREQUENCY STANDARD

Jacques Audet
VE2AZX

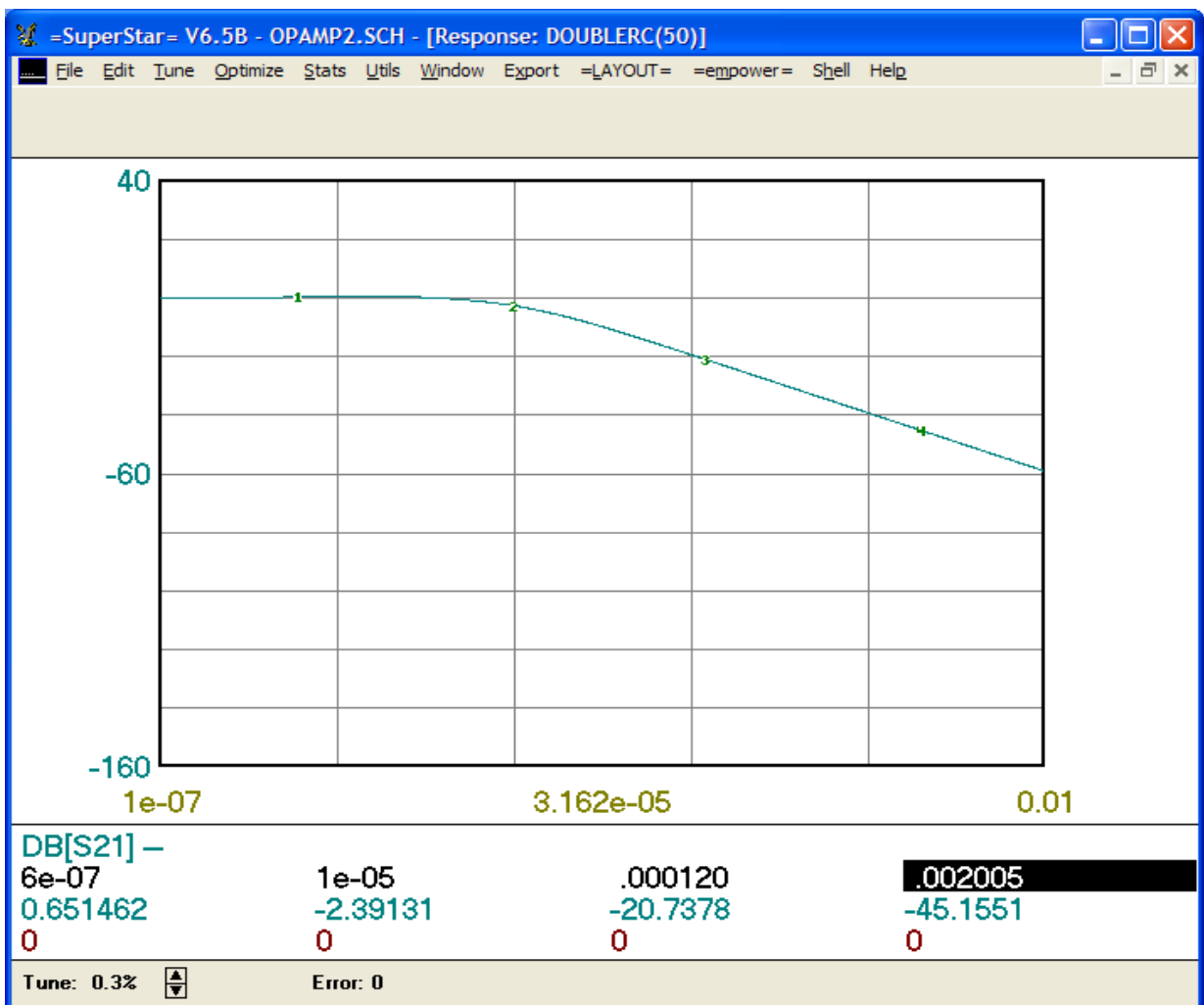
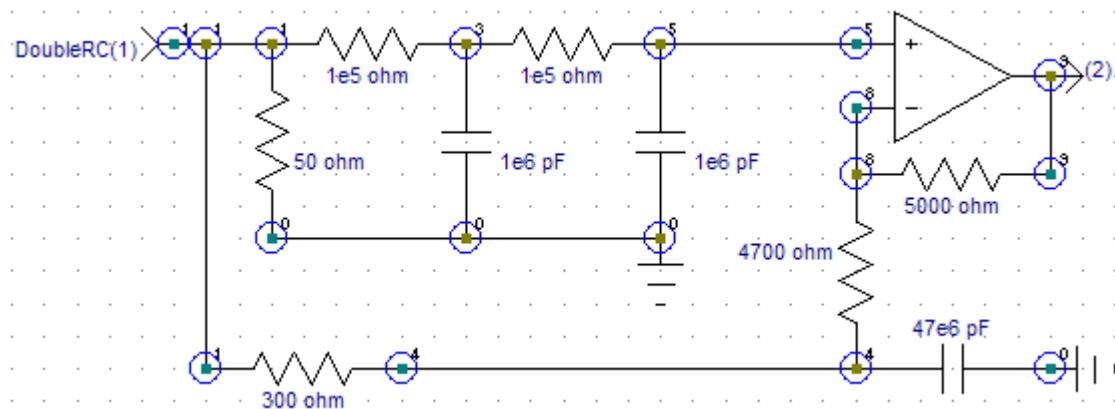


Spurious coming from PWM 5 V p-p drive



Notes: We have 118 dB atten at 2 KHz, With 5 V p-p input, gives 2.2 uV rms at output, 1.4 uV was measured (-85 dB below 25 mV)

Spurious coming from 120 Hz regulator ripple



Above circuit gives -20.7 dB attenuation at 120 Hz.

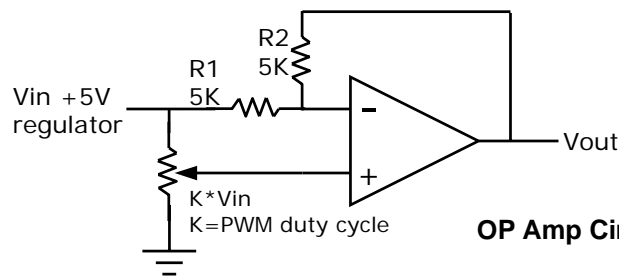
The 7805, 5 V regulator has ~ 1 V p-p ripple at input (measured)

Spec: 80 dB rejection, giving 0.1 mV p-p at output or 35 μ V RMS

This is attenuated 20.7 dB, (provided by $300\ \Omega + 47\ \mu$ F) giving: 3.2 μ V RMS

Measured value = 2.5 μ V

Frequency shifts caused by +5V DC stability



OP Amp Circuit Simulation – DC to ~ 0.1 Hz

A divider with a transfer value of K feeds the + input
(represents the PWM duty cycle)

$$R1 := 5000$$

$$R2 := 5000$$

$$V_{in} := 5$$

$$V_{out} := 0.29$$

Operating value

$$V_{out} = \frac{V_{in} \cdot K \cdot (R1 + R2)}{R1} - V_{in} \frac{R2}{R1} \quad \text{Gain equation}$$

Solve for K

$$K = \frac{(V_{out} \cdot R1 + V_{in} \cdot R2)}{[V_{in} \cdot (R1 + R2)]}$$

$$K := \frac{V_{out} \cdot R1 + V_{in} \cdot R2}{V_{in} \cdot (R1 + R2)} \quad K = 0.529$$

Plug the K value into the gain equation:

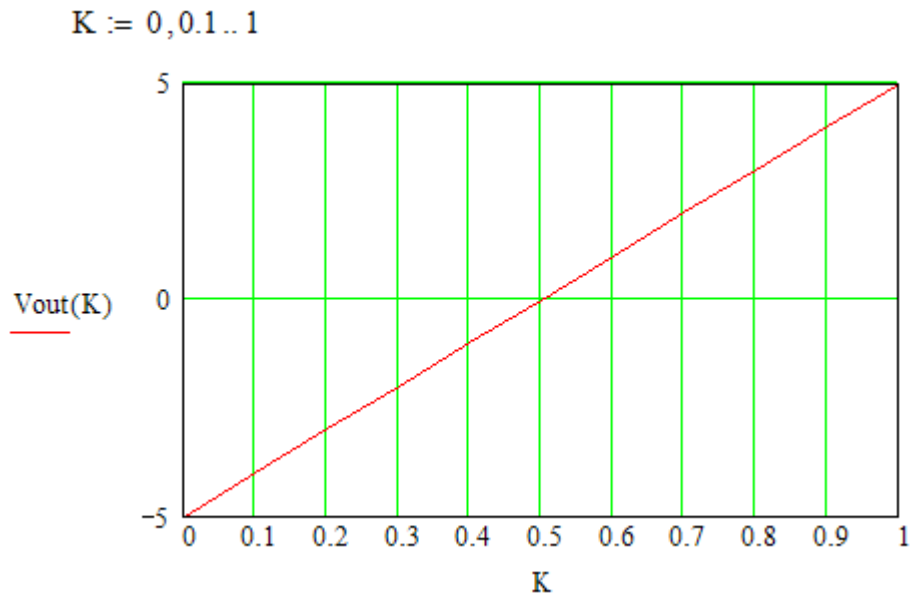
$$V_{out}(K) := \frac{V_{in} \cdot K \cdot (R1 + R2)}{R1} - V_{in} \cdot \frac{R2}{R1} \quad V_{out}(K) = 0.29$$

$$\frac{V_{out}}{V_{in}} = \frac{K \cdot (R1 + R2) - R2}{R1} \quad \text{Slope of gain equation}$$

$$\text{Slope} := \frac{K \cdot (R1 + R2) - R2}{R1} \quad \text{Slope} = 0.058$$

The output changes 0.058 mV per mV change of the +5V input (regulator)

DAC Vout vs K value (duty cycle)



- When $K = 0.5$, the output shows zero change. Should try to operate around $K=0.5$
- The output varies from $-5V$ to $+5V$ as K varies from 0 to 1 (100 % duty cycle)

Tuning Sensitivity: 5 V change causes 1 Hz frequency change of the 10 MHz VCXO

$TS := 0.2$ Tuning Sensitivity in Hz / V

$$\Delta f(\Delta V) := TS \cdot \Delta V \quad \text{In Hz at 10 MHz}$$

$$\Delta F12(\Delta V) := \frac{TS \cdot \Delta V}{10^7} \cdot 10^{12} \quad \text{In parts in } 10^{12}$$

$$\Delta F12(58 \cdot 10^{-6}) = 1.16$$

Freq change in parts in 10^{12} caused by a 1 mV change in the +5V supply, when $V_{out} = V_{DAC} = 0.29$ V

$$\Delta F12(2.5 \cdot \sqrt{2} \cdot 10^{-6}) = 0.071$$

Peak Freq change in parts in 10^{12} caused by 2.5 μ V RMS on the VCXO control voltage

Calculate the Spurious levels on the 10 MHz VCXO

$F_{\text{spur}} := 120$

Freq of spurious observed on the VCXO control

$$M_Index(\Delta V, F_{\text{spur}}) := \frac{\Delta f(\Delta V)}{F_{\text{spur}}} \quad \text{Modulation Index}$$

$$SB_Level(\Delta V, F_{\text{spur}}) := 20 \cdot \log\left(\frac{M_Index(\Delta V, F_{\text{spur}})}{2}\right) \quad \text{Sideband level in dBc}$$

$$SB_Level\left[\left(2.5 \cdot \sqrt{2} \cdot 10^{-6}\right), F_{\text{spur}}\right] = -170.615 \quad \begin{array}{l} \text{Sideband level in dBc} \\ \text{for 2.5 uV, 120 Hz spur} \end{array}$$

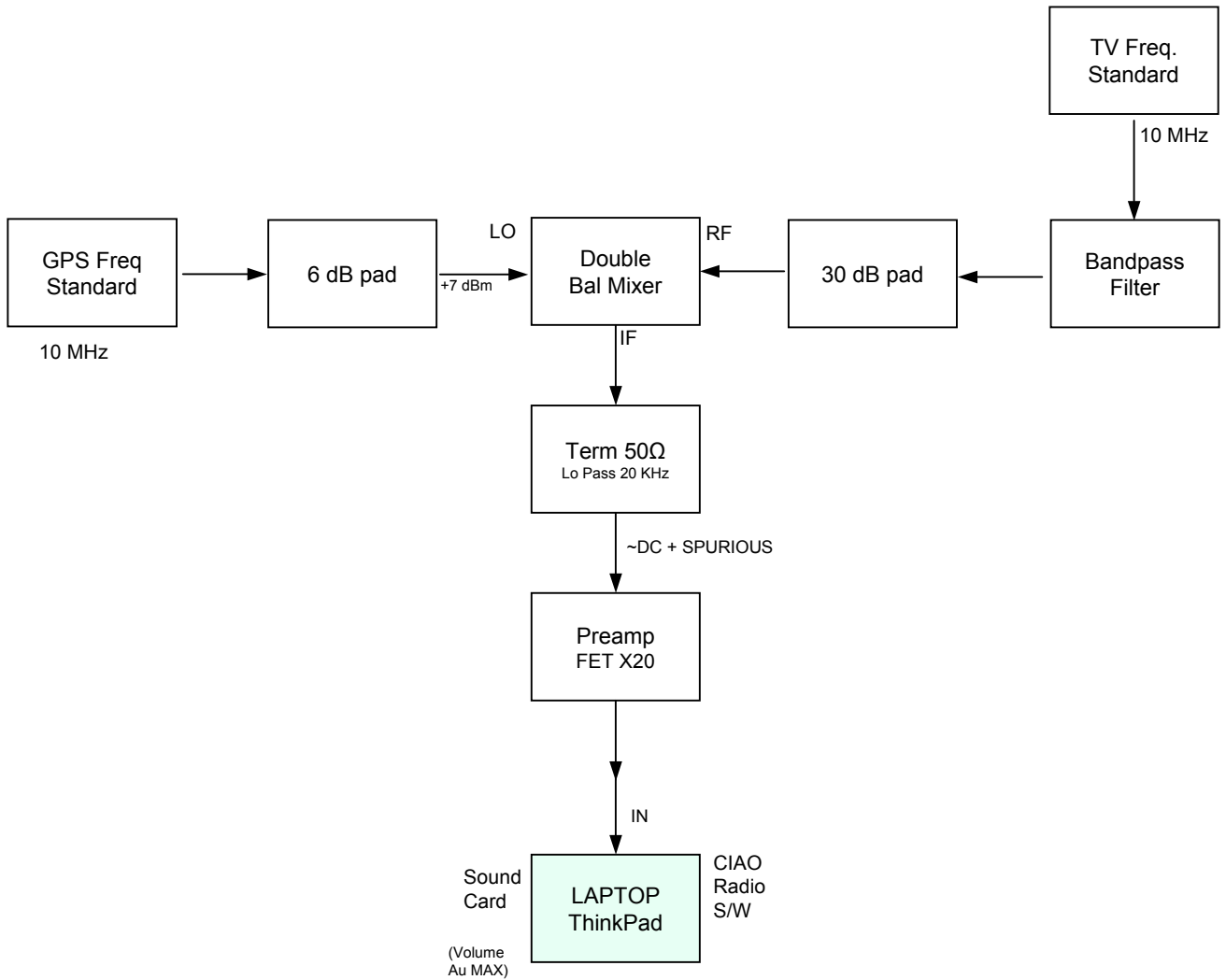
$F_{\text{spur}} := 2000$

Freq of spurious observed on the VCXO control

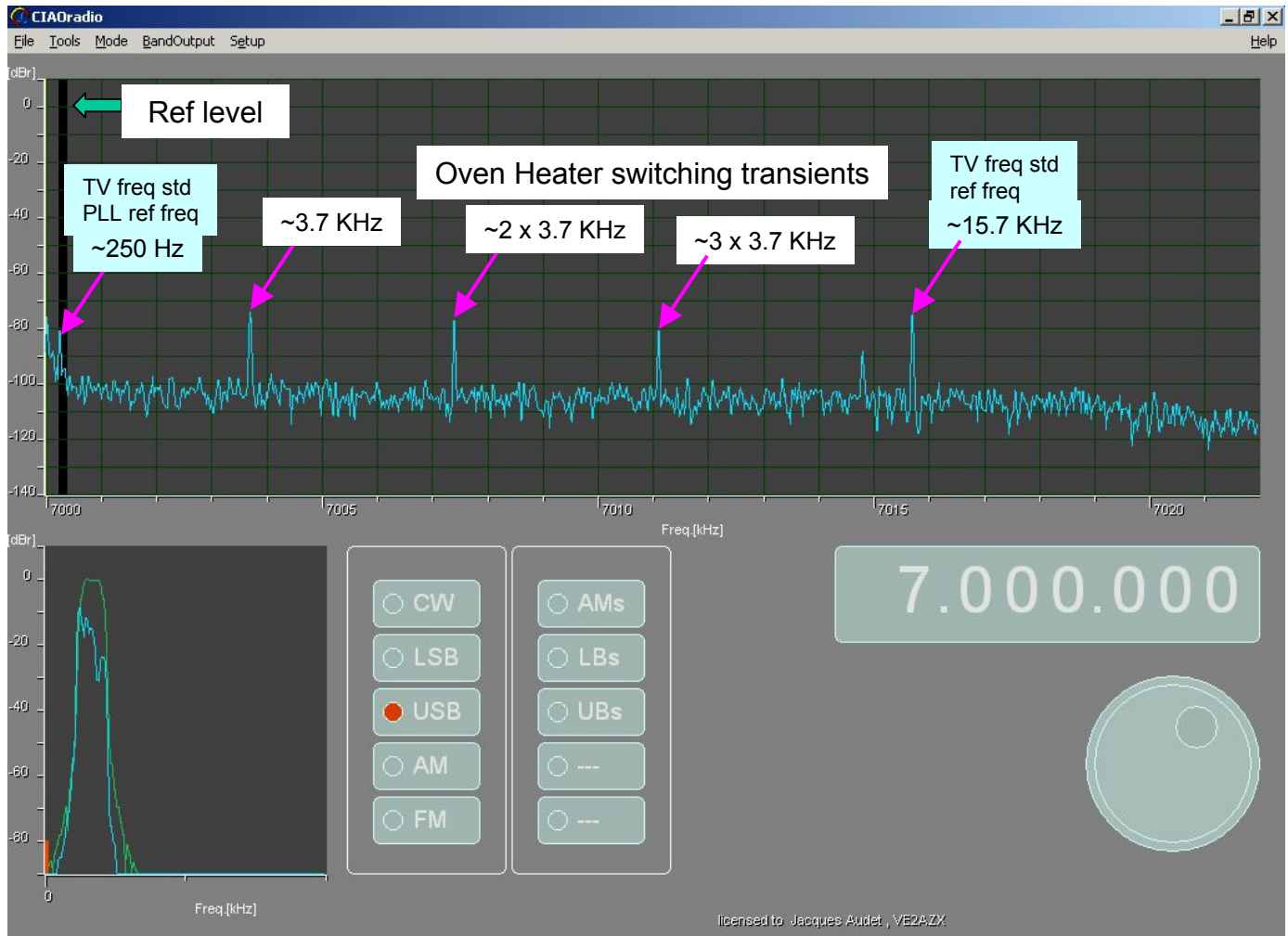
$$SB_Level\left[\left(2.5 \cdot \sqrt{2} \cdot 10^{-6}\right), F_{\text{spur}}\right] = -195.051$$

**No spurious were observed at these frequencies.
Only oven heater transients were present.**

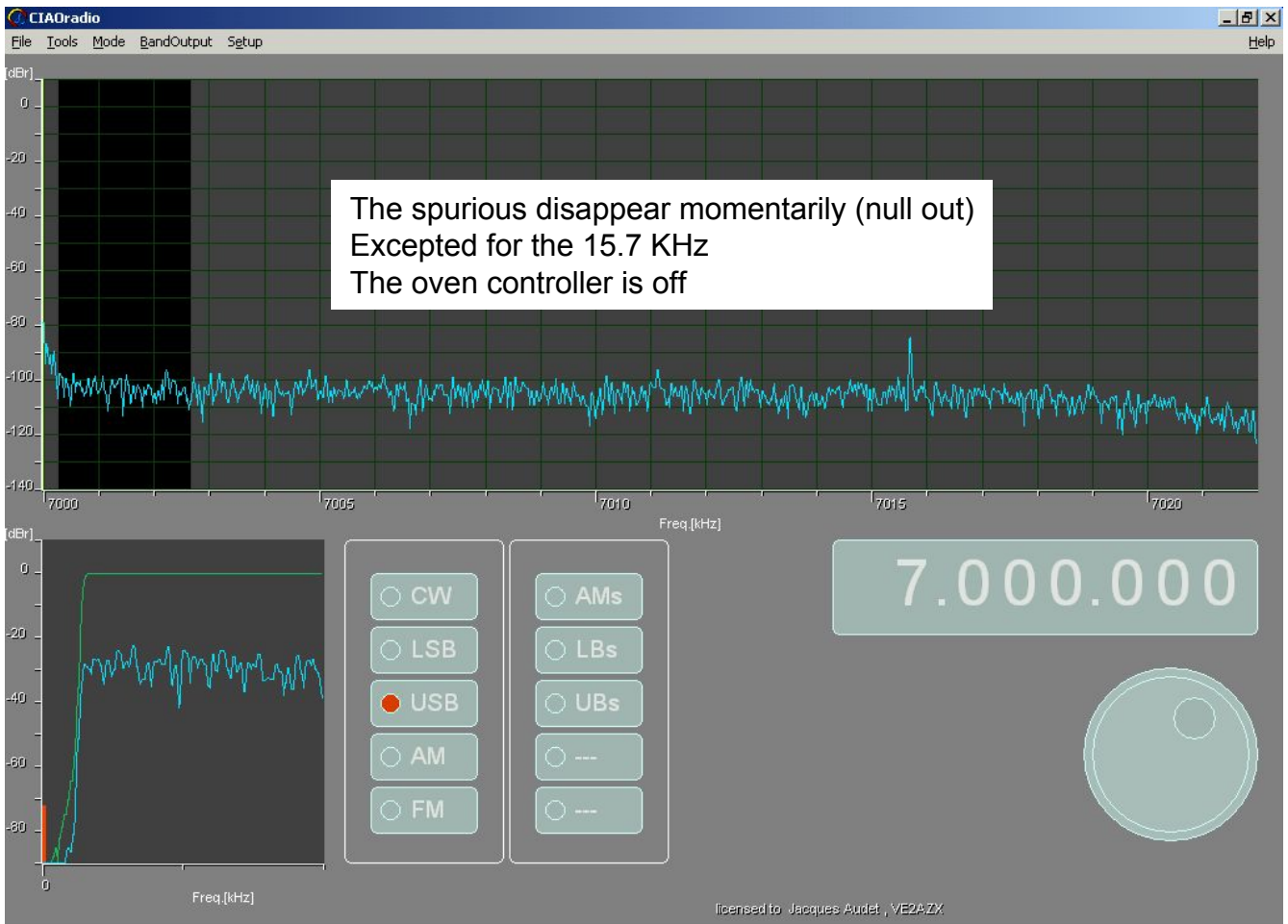
SET UP USED TO MEASURE 10 MHz SPURIOUS



MEASURING 10 MHz SPURIOUS

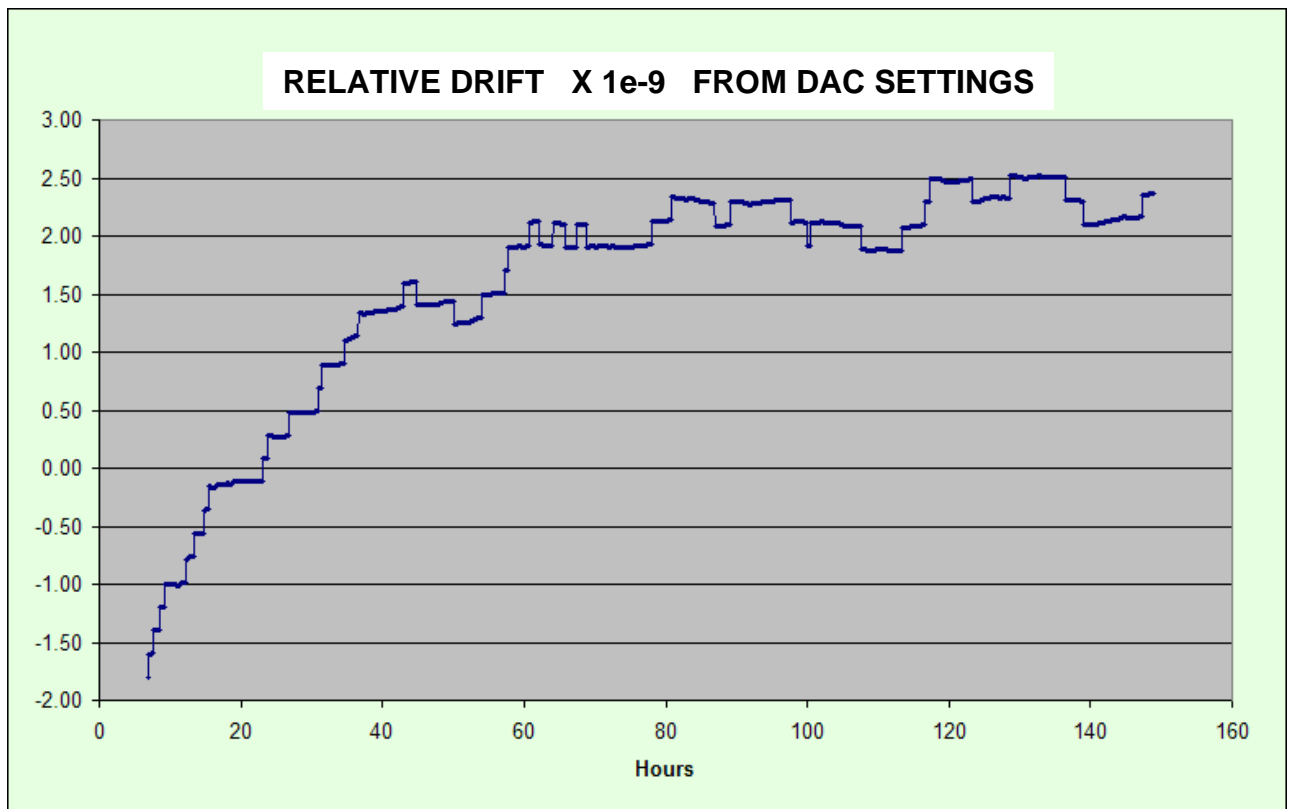


MEASURING 10 MHz SPURIOUS



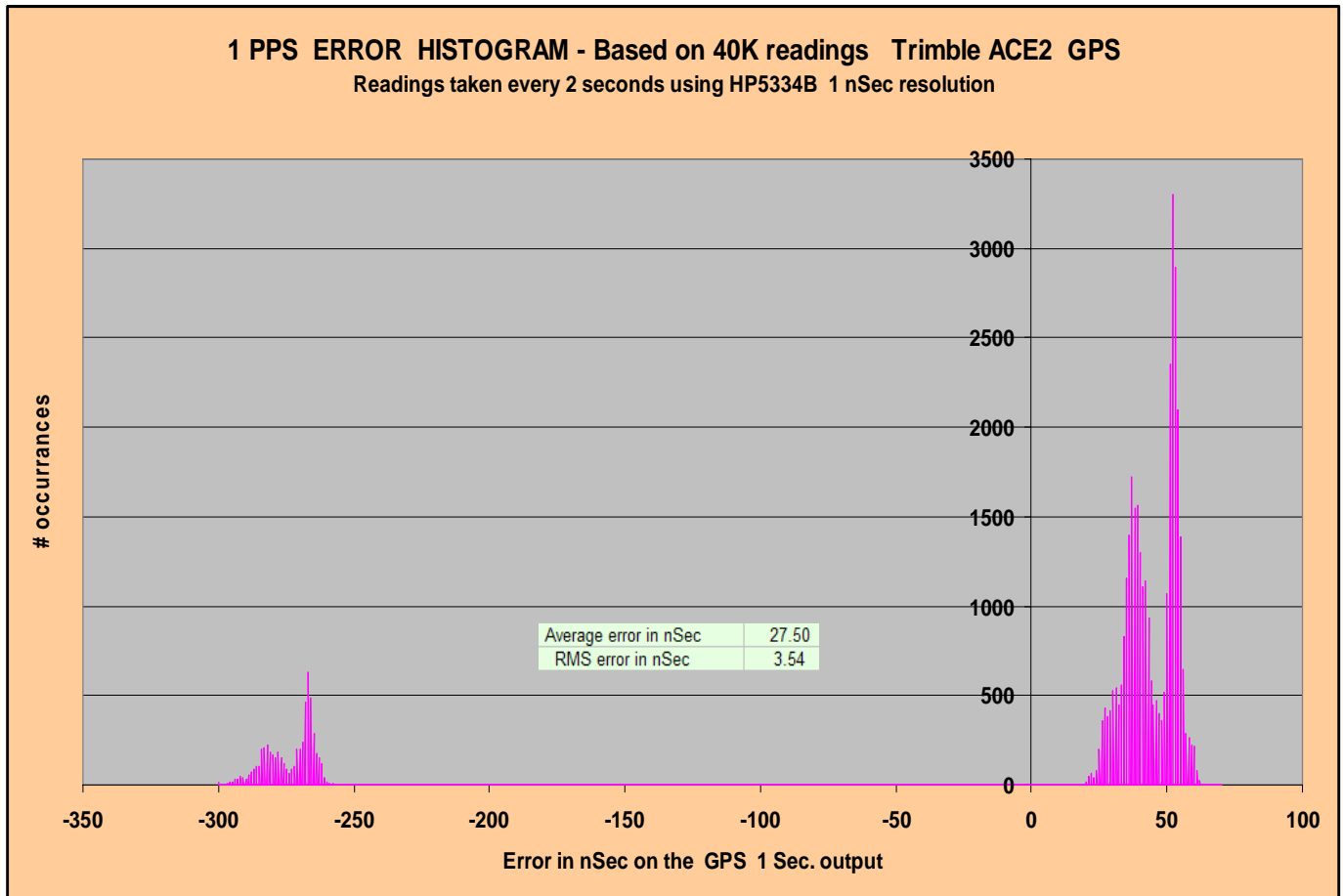
What the data sheet says:

To maximize oven-efficiency in the HP10544A, the heater current is controlled by a switching regulator circuit. This produces switching transients at about 4 kHz on the input line and a low level spurious signal on the output. A version of the oscillator with a dc oven controller is available. It should be used when adequate input filtering is difficult or better than -80 dB nonharmonic components on the output are required.



Drift data accumulated with the HP10544A VCXO, from a cold start.
At least 80 hours were required for the DAC to settle close to its final value.
Note that the relative drift was computed from the DAC value.

Measurements on the 1 pps timing



Here is what the Trimble Ace II reference manual says:

The timing accuracy is ± 100 nanosecond (1σ) and is available only when valid position fixes are being reported. Repeatability checks of 10 sets of 100 one second samples taken over a period of 20 minutes showed an average variation of approximately 100 nanoseconds (not allowing for SA).